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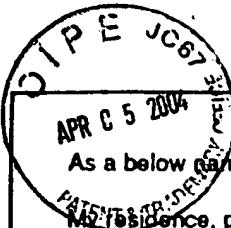
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## DECLARATION FOR PATENT APPLICATION

Docket Number (Optional)

4057

As above named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD AND APPARATUS FOR ACCELERATING THE VERIFICATION OF APPLICATION SPECIFIC INTEGRATED CIRCUIT DESIGNS, the specification of which

is attached hereto unless the following box is checked:

was filed on October 14, 2003 as United States Application Number or PCT International Application Number 10/686,022 and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Claimed

Yes  No

Yes  No

Yes  No

(Number)

(Country)

(Day/Month/Year Filed)

(Number)

(Country)

(Day/Month/Year Filed)

(Number)

(Country)

(Day/Month/Year Filed)

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

(Application Number)

(Filing Date)

(Status – patented, pending, abandoned)

(Application Number)

(Filing Date)

(Status – patented, pending, abandoned)

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

David O'Reilly

Address all telephone calls to David O'Reilly at telephone number (805) 446-2759

Address all correspondence to 1800 Bridgegate Street

Suite 200

Westlake Village, California 91361

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor (given name, family name)

Stanley M. Hyduke

Inventor's signature

Date 04-02-2004

Residence

Citizenship U.S.A.

Post Office Address 59 Gulf Stream Court  
Las Vegas, Nevada 89113

Full name of second joint inventor, if any (given name, family name)

Date

Second Inventor's signature

Citizenship

Residence

Post Office Address



Additional inventors are being named on separately numbered sheets attached hereto.

## DECLARATION FOR PATENT APPLICATION

Docket Number (Optional)  
4057

As a below named Inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole Inventor (if only one name is listed below) or an original, first and joint Inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled  
METHOD AND APPARATUS FOR ACCELERATING THE VERIFICATION OF APPLICATION SPECIFIC INTEGRATED CIRCUIT DESIGNS,, the specification of which is attached hereto unless the following box is checked:

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## Prior Foreign Application(s)

(Number)	(Country)	(Day/Month/Year Filed)	Priority Claimed <input type="checkbox"/> Yes <input type="checkbox"/> No
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(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No

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Full name of sole or first inventor (given name, family name)

Inventor's signature \_\_\_\_\_ Date \_\_\_\_\_ Citizenship \_\_\_\_\_

Residence \_\_\_\_\_

Post Office Address \_\_\_\_\_

Full name of second joint inventor, if any (given name, family name)

Second Inventor's signature Slawomir Grabowski Date 05-04-2004 Citizenship Poland

Residence \_\_\_\_\_

Post Office Address Lakta Gorna 248

32-731 Zegocina, Poland

Additional Inventors are being named on separately numbered sheets attached hereto.